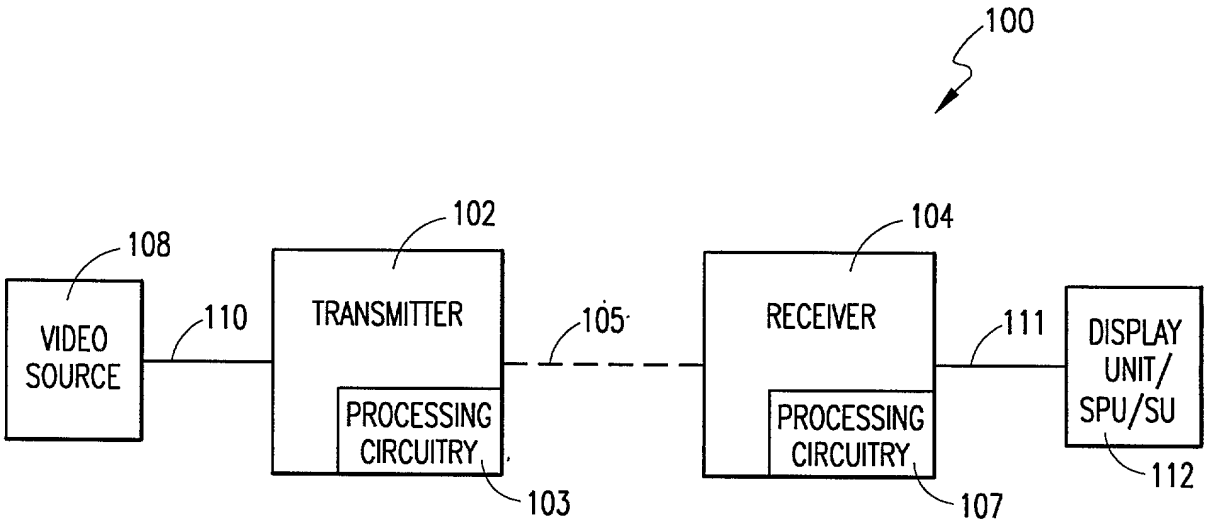

100
108
VIDEO SOURCE
110
102
TRANSMITTER
PROCESSING CIRCUITRY
103
105
104
RECEIVER
PROCESSING CIRCUITRY
107
111
DISPLAY UNIT/ SPU/SU
112

*FIG. 1*

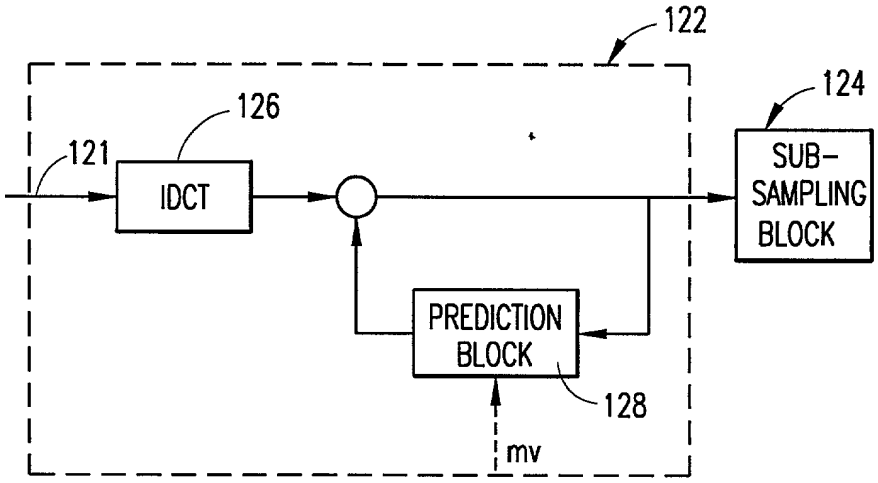



*FIG. 2*
(PRIOR ART)

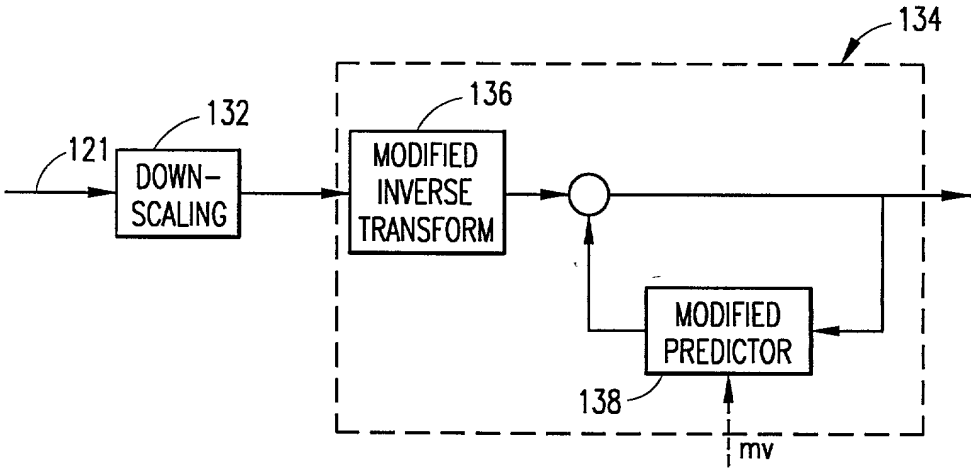



*FIG. 3*

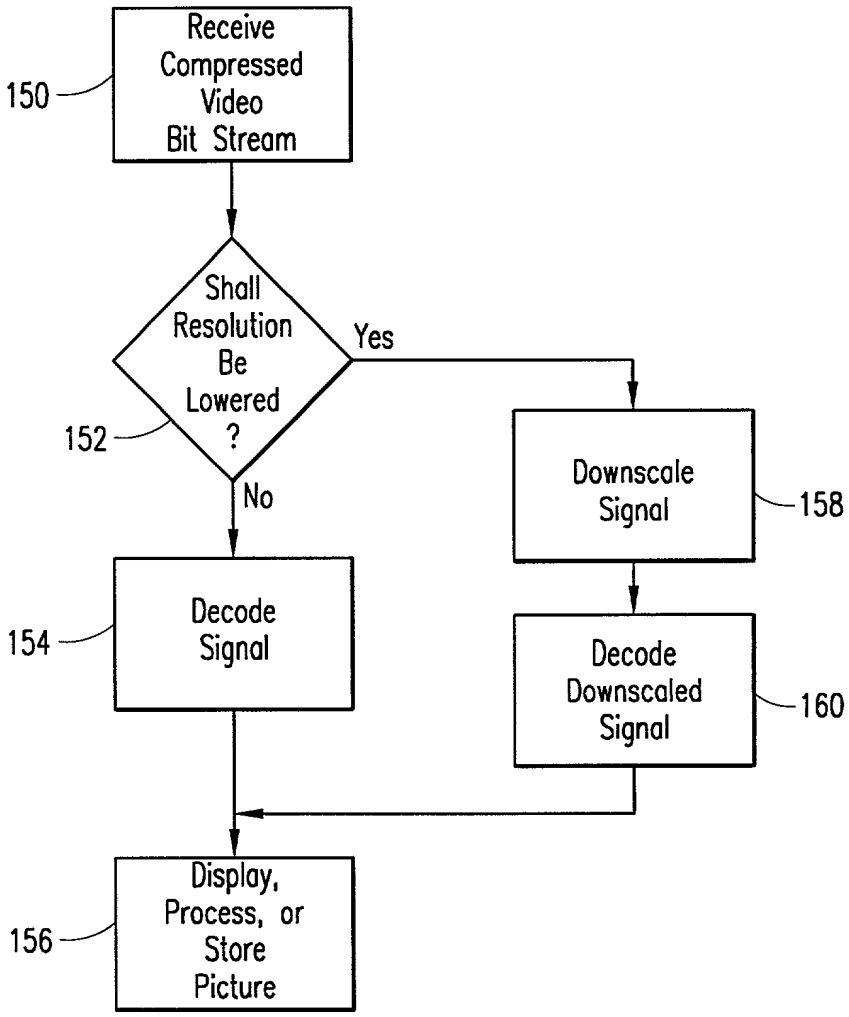

Receive Compressed Video Bit Stream
150
Shall Resolution Be Lowered ?
152
Yes
No
Downscale Signal
158
Decode Signal
154
Decode Downscaled Signal
160
Display, Process, or Store Picture
156

*FIG. 4*